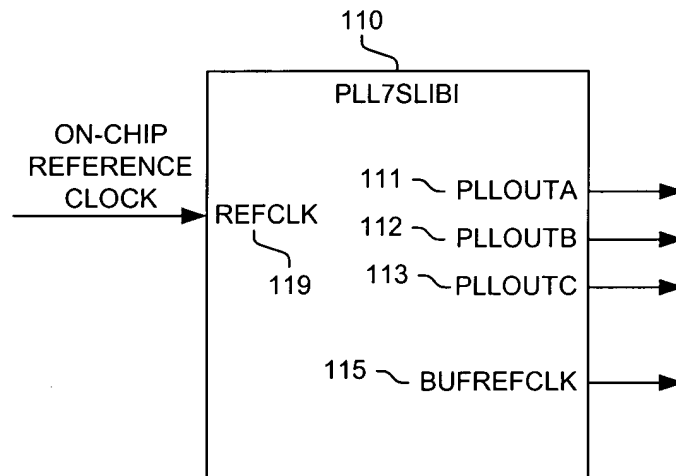


**PLL7SLIBE
PRIOR ART
FIGURE 1A**



**PLL7SLIBI
PRIOR ART
FIGURE 1B**

PLL LOW-LEVEL BLOCK DIAGRAM
PRIOR ART
FIGURE 1C

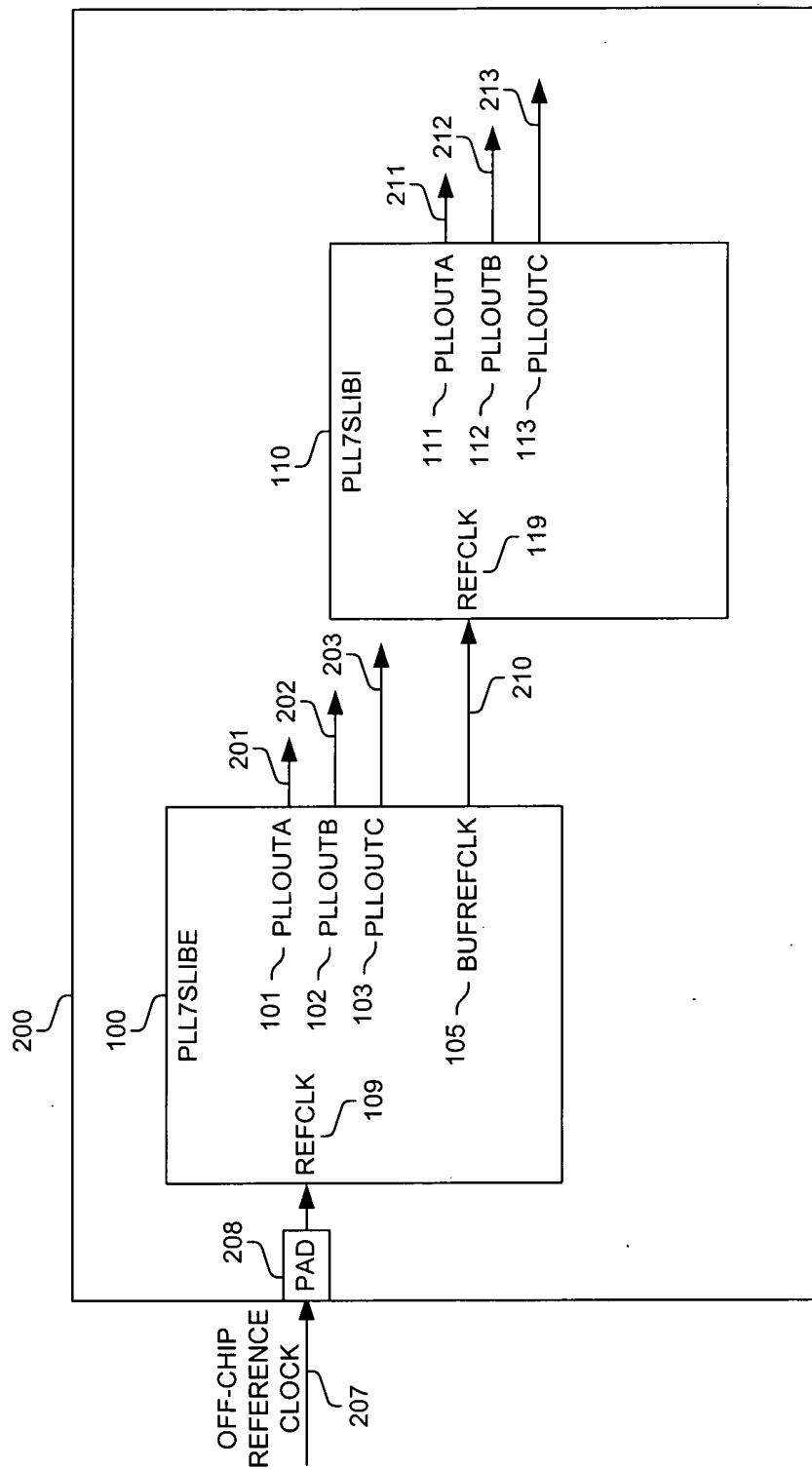


FIGURE 2

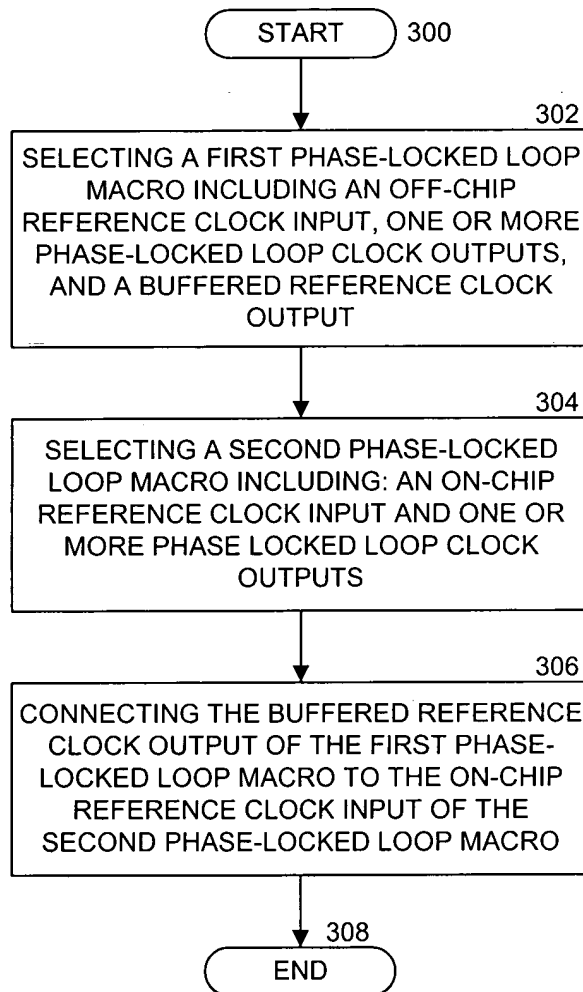


FIGURE 3A

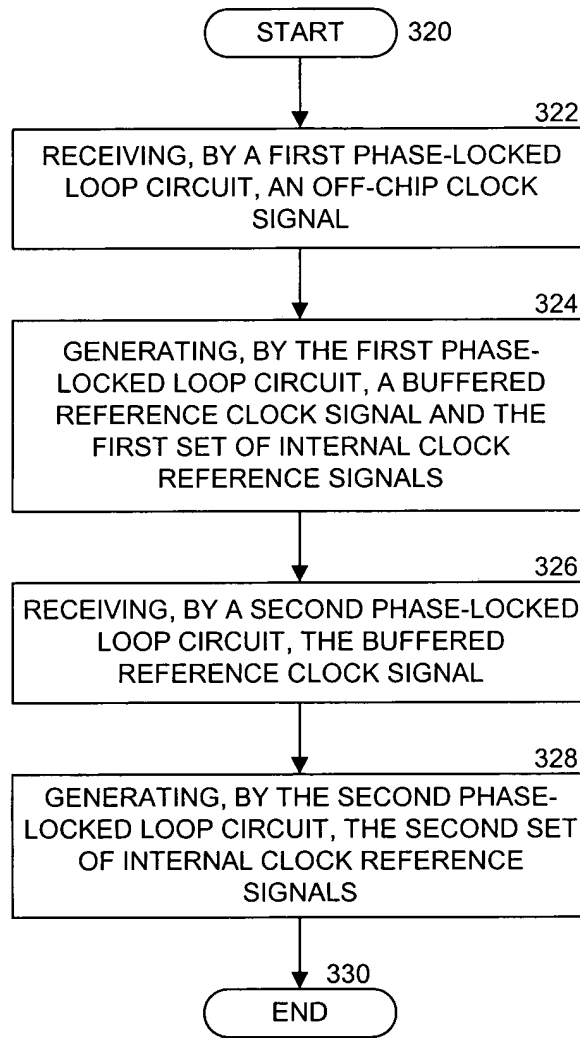


FIGURE 3B